L Number	Hits	Search Text	DB	Time stamp
-	527	complex adj programmable adj logic adj device\$1	USPAT;	2003/02/04 14:01
	1		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(complex adj programmable adj logic adj device\$1) same (dsp	USPAT;	2003/02/04 14:03
		processor microprocessor cpu (digital adj signal adj	US-PGPUB;	
		controller)) same sdram same ((media adj access adj control)	EPO; JPO;	
		fifo) same network	DERWENT;	
	_		IBM_TDB	
-	0	(complex adj programmable adj logic adj device\$1) same (dsp	USPAT;	2003/02/04 14:03
		processor microprocessor cpu (digital adj signal adj	US-PGPUB;	
		controller)) same sdram same ((media adj access adj control)	EPO; JPO;	
		fifo) and network	DERWENT;	
	_		IBM_TDB	
-	0	((complex adj programmable adj logic adj device\$1) same	USPAT;	2003/02/04 14:04
		(dsp processor microprocessor cpu (digital adj signal adj	US-PGPUB;	
İ		controller)) same sdram same ((media adj access adj control)	EPO; JPO;	
		fifo)) and network	DERWENT;	
			IBM_TDB	
-	1	((complex adj programmable adj logic adj device\$1) same	USPAT;	2003/02/04 14:05
		(dsp processor microprocessor cpu (digital adj signal adj	US-PGPUB;	
		controller)) same (memory dram sdram) same ((media adj	EPO; JPO;	
		access adj control) fifo)) and network.	DERWENT;	
			IBM_TDB	
-	238	read same write same simultaneous\$2 same dma	USPĀT;	2003/02/04 14:07
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(read same write same simultaneous\$2 same dma) and ((read	USPAT;	2003/02/04 14:08
		adj3 (dsp procesor)) same (write adj3 memory))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	455		IBM_TDB	
-	453	(data adj recorder) and instrument	USPĀT;	2003/02/04 14:56
	•		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5392	data adj recorder	USPAT;	2003/02/04 14:56
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		(1)	IBM_TDB	0007/00/04 4 4 57
-	0	(data adj recorder) same (memory sdram) same (mca fifo)	USPAT;	2003/02/04 14:57
		same (microprocessor processor cpu dsp) same	US-PGPUB;	
		simultaneous\$2	EPO; JPO;	
			DERWENT;	
	7.0	(many adaptive of the control of the	IBM_TDB	0007/00/04 44 77
-	360	(memory sdram) same (mca fifo) same (microprocessor	USPAT;	2003/02/04 14:59
		processor cpu dsp) same simultaneous\$2	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	~	4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4	IBM_TDB	
-	314	((memory sdram) same (mca fifo) same (microprocessor	USPAT;	2003/02/04 14:59
		processor cpu dsp) same simultaneous\$2) and	US-PGPUB;	
		@ad < 20000929	EPO; JPO;	
			DERWENT;	
	İ		IBM TDB	1

•	68	(memory sdram) same (mca fifo) same (microprocessor	USPAT;	2003/02/04 15:01
1		processor cpu dsp) same simultaneous\$2 same read same	US-PGPUB;	
		write	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	54	(() data (USPAT;	2003/02/04 15:02
		processor cpu dsp) same simultaneous\$2 same read same	US-PGPUB;	
		write) and @ad < 20000929	EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	84	(inclopiocessoi	USPAT;	2003/02/04 15:02
		processor cpu dsp) same (concurrent\$2 simultaneous\$2)	US-PGPUB;	
		same read same write	EPO; JPO;	
		·	DERWENT;	
			IBM TDB	
-	68	((memory sdram) same (mca fifo) same (microprocessor	USPAT;	2003/02/04 16:23
		processor cpu dsp) same (concurrent\$2 simultaneous\$2)	US-PGPUB;	2003/02/01 10.23
		same read same write) and @ad < 20000929	EPO; JPO;	
		,	DERWENT;	
			IBM TDB	
-	342	(read same write) same simultaneously same different same	USPAT;	2003/02/04 16:23
		device\$1	US-PGPUB;	2003/02/04 10:23
		,	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	232	((read same write) same simultaneously same different same		2007/00/04 17 71
		device\$1) and @ad < 20000929	USPĀT;	2003/02/04 16:31
		device \$1) and @ad < 20000727	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
-	30	(((read same write) same simultaneously same different same	IBM_TDB	2007/20/20/
	30	device\$1) and @ad < 20000929) and (different adj3	USPAT;	2003/02/04 16:32
		device\$1) and wad < 20000929) and (different adjs	US-PGPUB;	
		device \$1)	EPO; JPO;	
			DERWENT;	
_	2363	DMA.ti.	IBM_TDB	
	2303	DITA.U.	USPAT;	2003/02/04 18:03
	.		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		DMA d and /a 1 10 10 11 11 11	IBM_TDB	
-	0	DMA.ti. and (complex adj programmable adj logic adj	USPAT;	2003/02/04 18:03
		device\$1)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
-	0	DMA.ti. and (((read same write) same simultaneously) same	USPĀT;	2003/02/04 18:05
		fifo same sdram same (cpu dsp processor microprocessor))	US-PGPUB;	
	1		EPO; JPO;	
İ	1		DERWENT;	
			IBM TDB	
-	0	DMA.ti. and (((read same write) same simultaneously) same	USPAT;	2003/02/04 18:05
	ŀ	fifo same memory same (cpu dsp processor microprocessor))	US-PGPUB;	
			EPO; JPO;	
]	ļ		DERWENT;	
	l		IBM TDB	
-	0	DMA.ti. and (((read and write) same simultaneously) same	USPĀT;	2003/02/04 18:06
		fifo same sdram same (cpu dsp processor microprocessor))	US-PGPUB;	2003/02/04 10:00
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
			מעו_ויומו	

•	939	media adj access adj controller	USPAT; US-PGPUB;	2003/02/05 08:58
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	104	(media adj access adj controller) same fifo	USPAT;	2003/02/05 08:52
		· ·	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	213	((media adj access adj controller) same fifo) non-addressable	USPAT;	2003/02/05 08:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
İ			IBM_TDB	
-	0	((media adj access adj controller) same fifo) and	USPAT;	2003/02/05 08:52
		non-addressable	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		to the set of the second to th	IBM_TDB	2007/02/05 02 50
-	60	(media adj access adj controller).ti.	USPAT;	2003/02/05 08:59
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	51	//modia adi access adi controller) ti) and @ad < 20000020	IBM_TDB	2007/02/05 00 01
-	31	((media adj access adj controller).ti.) and @ad < 20000929	USPAT;	2003/02/05 09:01
		and (media adj access adj controller)	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM TDB	
_	7	((media adj access adj controller).ti.) and @ad < 20000929	USPAT;	2003/02/05 13:53
-	,	and ((media adj access adj controller) same fifo)	US-PGPUB;	2003/02/03 13:33
		and ((media ad) access adj condoner) same moj	EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	474	non-addressed	USPAT;	2003/02/05 13:53
			US-PGPUB;	2000,02.00
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	207	non-addressed same (device\$1 memory)	USPĀT;	2003/02/05 09:09
		, , , , , , , , , , , , , , , , , , , ,	US-PGPUB;	
-			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	11	non-addressed same fifo	USPĀT;	2003/02/05 09:42
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
-	28	cache same miss same simultaneously same read same write	USPAT;	2003/02/05 09:44
		same (ram memory sdram dram) same controller	US-PGPUB;	
		·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0007/00/07 := =
-	27	(cache same miss same simultaneously same read same write	USPAT;	2003/02/05 10:37
		same (ram memory sdram dram) same controller) and	US-PGPUB;	
		@ad < 20000929	EPO; JPO;	
-			DERWENT;	
			IBM_TDB	

O ((media adj access adj controller).ti.) and @ad < 20000929 uSPAT; and ((media adj access adj controller) same fifo same non-addressed) EPO; JPO; DERWENT; IBM TDB	2/05 14:16
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